

DESCRIPTION

METHOD AND DEVICE FOR WAVELET DENOISING

5 The present invention relates to a digital receiver and related method of receiving digital signals.

Digital broadcast systems such as the Coded Orthogonal Frequency Division Multiplexing (COFDM) have found particular use in Digital Audio
10 Broadcasting (DAB) and Digital Terrestrial Television (DVB-T).

In such a broadcast system, the bits of each digital sample are distributed over a number of carriers and at a number of different time intervals. The carriers are each at a different evenly-spaced frequency within the transmission channel. The waveform in each time interval is normally
15 created by an Inverse Fast Fourier Transform (IFFT) of the amplitude and phase values required for the carriers during that time interval.

The output of each IFFT creates a so-called symbol, which has duration of a symbol period. A plurality of such symbols are combined so as to form a COFDM frame. The COFDM frames are then transmitted and the distribution
20 of bits of data across frequency and time provides for a system which is advantageously error resistant and can also cope with a high degree of multipath distortion.

In the receiver's demodulator a Fast Fourier Transform (FFT) is performed on the waveform in each symbol to determine the amplitude and
25 phase of the carriers of which it is comprised. From these amplitude and phase values the data carried by the symbol is derived.

COFDM is a version of a communications system referred to as Orthogonal Frequency Division Multiplexing (OFDM). The features of which are described in greater detail in European Standard EN 300 744, V1.1.2
30 (1997-8), Digital Video Broadcasting (DVB); Framing Structure, channel coding and modulation for digital terrestrial television, published by European

Telecommunications Standard Institute (ETSI) Valbonne, France, August 1997.

Disadvantages do however arise in that the sequentially transmitted symbols can be severely affected by short term noise events that can serve to
5 distort each individual symbol. For example, COFDM based systems such as those discussed above, can be disrupted by strong interference from traffic-related switching events or high current switching events arising within domestic environments.

It would therefore be advantageous to try and limit the effect of noise,
10 such as impulsive interference events, within digital signal transmission systems.

EP-A-1043874 discloses a COFDM receiver arrangement that seeks to limit the effect of impulsive interference events and in which the magnitude of a received signal is compared to threshold levels in an attempt to identify the
15 presence of impulsive interference. If such threshold value is exceeded, then that portion of the signal exceeding the threshold is replaced by a zero value in an attempt to limit the effect of the interference.

However, such a system is disadvantageously limited insofar as its operation depends upon a mere comparison of magnitude and threshold value
20 and so an inaccurate determination of the presence of impulsive interference events can prove common. Also, the mere replacement of a likely interference impulse merely by a zero value does not necessarily lead to a fully accurate recreation of the original signal absent to the interference impulse.

25 The present invention therefore seeks to provide for a digital receiver arrangement, and related method, having advantages over known such arrangements and methods.

According to a first aspect of the present invention, there is provided a
30 digital receiver arrangement comprising a tuner/demodulator circuit and analogue-to-digital converting means, and further including means for storing an impulse wavelet representation, means for determining if an interference

impulse is present in a received signal, and means for introducing the stored representation of the impulse wavelet to the detected received impulse so as to counteract the effect thereof within the received signal.

The invention is advantageous in providing means for accurately
5 seeking to remove an interference impulse from an incoming digital signal and in a manner having limited effect on the remainder of the signal.

The feature of Claim 2 represents a particularly accurate and efficient means for determining the arrival of an impulse interference event within a received signal and the features of the Claims 3 and 4 comprise advantageous
10 embodiments of such means.

The feature of Claim 5 is advantageous in offering a particularly simple, and effective means serving to remove the impulse interference event from the received signal.

The feature of Claim 6 proves particularly advantageous in generating a
15 stored representation of an impulse infringement event that can offer a high degree of accuracy when compared with an incoming impulse interference event in the received signal.

The features of Claim 10 and 11 prove particularly advantageous in enhancing the accuracy with which an impulse interference event can be
20 effectively removed from the received incoming digital signal.

According to another aspect of the present invention, there is provided a method of receiving a digital signal including the steps of demodulating the signal, and conducting an analogue-to-digital conversion of the signal, and including the further steps of storing an impulse wavelet representation,
25 determining if an impulse interference event is found within an incoming signal, and introducing the said stored wavelet representation to the received impulse interference event so as to counteract the effect thereof.

As will therefore be appreciated, the present invention provides for means for detecting the occurrence of an elementary impulsive interference
30 event by identifying a sequence of perturbation to the received data characteristic of the effect of applying a fast voltage step to the input of the receiver system. Particularly advantageously, means are provided for

characterising the data perturbation as expected from an elementary impulsive interference event by an automatic learning process performed, for example, when the digital receiver is first commissioned or each time it is switched on.

A means for reducing the effect of an elementary impulsive interference event by subtracting a scaled version of the data perturbation sequence
5 respected from such an event is therefore achieved.

It will of course be appreciated that the present invention can readily be incorporated into integrated circuits provided for demodulating DVB-T signals in order to reduce the effect of such interference.

10 The invention thus provides for a receiver with improved immunity to impulsive interference events and so finds ready use particularly in DAB and DVB-T systems.

The invention is described further hereinafter, by way of example only,
15 with reference to the accompanying drawings in which:

Fig. 1 represents a schematic block diagram of a known digital receiver system and so as to illustrate the manner in which an impulsive interference event arises; and

Fig. 2 is a schematic block diagram of a receiver arrangement
20 according to an embodiment of the present invention.

Turning first to Fig. 1 there is illustrated a block diagram of a typical digital receiver arrangement 10 for receiving digital wireless signals and which comprises an antenna 12 connected to a front-end system 14, which typically
25 includes tuning and filtering devices. The front-end system 14 is in turn connected to analogue-to-digital conversion means 16, which delivers an output digital signal to the remainder of the demodulation system 18.

In turn, this remainder of the demodulation system 18 provides for a digital data output 20.

30 The front-end system 14 serves to translate the desired range of frequencies to a required intermediate frequency range and also serve to attenuate other frequencies.

Also illustrated within Fig. 1 is an incoming impulsive interference event, which could disadvantageously result in one or more bursts of pulses at the output of the front-end system 14. It should be appreciated that each pulse within a burst is usually caused by a fast interference voltage edge received at the antenna 12 and an example of such a fast interference voltage edge 22 is
5 also illustrated in Fig. 1. Such edges and their consequences comprise elementary impulsive interference events.

Although a signal edge, or a train of edges, might appear at the antenna 12, each will produce a waveform 24 such as that illustrated at the output side of the front-end system 14 in Fig. 1. The amplitude and shape of this waveform
10 24 is determined by the time response of the antenna 12 and the front-end system 14. The waveform 24 can be thought of as comprising an impulse wavelet. Such an impulse wavelet 24 will arise in addition to the desired signal received at the antenna and at the output of the analogue-to-digital converter
15 16, the voltage edge 22 received at the antenna 12 has by then served to create a sequence of data perturbation values 26 of the wavelet 24 such as those also illustrated in Fig. 1.

In practice, such data perturbation values 26 will be complex in nature and so have real and imaginary parts and will also be dependent upon the
20 sampling process. In principle however, the additional impulse wavelet maybe identifiable within the data and, once it has been assessed, it can be subtracted from the received signals to provide for improved accuracy of reproduction of the received signal.

Turning now to Fig. 2, there is illustrated a schematic block diagram of a
25 receiver arrangement 28 according to one embodiment of the present invention.

The receiver arrangement 28 of Fig. 2 includes some of the typical features illustrated in Fig. 1 and so, where appropriate, common reference numerals have been employed.

30 Thus, it will be appreciated that the receiver arrangement embodying the present invention also comprises an antenna 12, front-end system 14, an

analogue-to-digital converter 16 and a block 18 representing the remainder of the demodulation system for providing a digital data output 20.

Importantly however, the embodiment of the present invention has additional features as discussed below.

5 An impulse wavelet learning system 30 is included for tapping into the output of the analogue-to-digital converter 16 so as to form an estimate of the shape of an impulse wavelet 24 likely to arrive at the analogue-to-digital converter 16. The operation of such a learning system 30 can include the application of a known test signal via a summation device 32, which serves to
10 receive signals from both the impulse wavelet learning system 30 and also the antenna 12, and then deliver an input to the front-end system 14. The known test signal can be created at the time that the arrangement 28 is designed, or commissioned, or indeed each time the arrangement 28 is switched on. In the alternative however, the wavelet shape maybe predicted from the known
15 characteristics of the arrangement 28, or by some other appropriate means. However the irrespective manner in which the estimate of the shape of the impulse wavelet is obtained, once created, the estimate of the wavelet is stored within a memory unit 34.

 During operation of the arrangement 28 of Fig. 2, any perturbation in
20 the received data due to an impulsive edge 22 received at the antenna 12 is detected and assessed by an impulse wavelet detection and assessment system 36 which, as with the impulse wavelet learning system 30, taps into the input of the analogue-to-digital converter 16. Such detection and assessment system 36 is arranged to compare continually the received data with the
25 impulse wavelet shape stored in the memory 34. In this manner, the detection and assessment system can employ a continuous cross-correlation, or optimal filtering, circuit. Once having detected an impulse wavelet perturbation within the signal output from the analogue-to-digital converter 16, the detection and assessment system 36 serves to assess the amplitude and phase of the
30 perturbation or, if dealing with a complex value, the real and imaginary components thereof are assessed.

Such assessment of wavelet amplitude and phase are then delivered to a multiplier 38, which is also arranged to receive an output of the estimate stored in the memory 34.

5 The multiplier 38 is also arranged to scale the stored wavelet shape to form a more accurate estimate of the identified interference pulse which is then subtracted from the incoming data within a summation device 40 so as to produce a less-corrupted data sequence which is then delivered to the remainder of the demodulation system 18.

10 Thus, as will be appreciated, the present invention provides for a relatively simple and effective means for producing a digital receiver system in which a fast interference voltage edge received at the antenna, such as an elementary impulsive interference event and which results in an unwanted waveform added to the required signal, allows for processing at the impulse wavelet level and which impulsive wavelet is removed from the front-end
15 processed and digitised signal so as to effectively remove the effect of the impulsive interference event.

The removal of the impulsive wavelet by means of an estimate of that wavelet serves to enhance the accuracy of the noise suppression offered by the present invention.